

## Parsivanath Charitable Brust's A. P. SIRIARI INSTRITUTE OF TECHNOLOGY

(Approved by AICTE New Delhi & Govt. of Maharashtra, Affiliated to University of Mumbai)
(Religious Jain Minority)

## Department of Electronics and Telecommunication Engineering Academic Year 2018-19

Date: 19 July 2018

## Report on Orientation program on "Microprocessors & Peripherals Interfacing"

Mumbai University has revised syllabus of engineering course from academic year 2016-17. Revised syllabus of third year engineering will be effective from academic year 2018-19. Microprocessor and peripheral Interfacing is newly introduces subject for fifth semester of Electronics & Telecommunication engineering.

On behalf of Mumbai University, department of Electronics & Telecommunication engineering has conducted orientation program on above mentioned subject on 18<sup>th</sup> July 2018. More than 25 faculties from various institutes of Mumbai University got benefitted through this program.

Prof. A. M. Deshpande (HOD, EXTC, APSIT, Thane) was convener and Prof. U. D. Kolekar (Principal, APSIT, Thane), Chairman, Board of Studies in Electronics & Telecommunication Engineering graced this orientation program.

Prof. U. D. Kolekar welcomes all participants of various institutes. He mentioned during his address that, suggestions from industry experts are taken to design this new subject.

Prof. A. M. Deshpande conveys the purpose and goal of newly introduces subject. He addressed that, main intention of this subject is to build essential concepts of microprocessor and develop the programming skill among the students rather than covering more number of topics.

Faculty members participating in orientation were much impressed with new syllabus and expressed their thoughts.



Prof. U. D. Kolekar (Principal, APSIT, Thane) expressing his views.



Prof. A. M. Deshpande (HOD, EXTC, APSIT, Thane) addressing the participants.